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Prof. Jack Luo received his PhD degree from the University of Hokkaido, Japan in 1989. He worked as a process engineer afterward at Hoxan Co. Sapporo, Japan, to develop high efficiency crystalline silicon solar cells. From 1990-1995, he worked as a research associate and senior research fellowship at Cardiff University, U.K on the electrical characterization of InP solar cells and GaAs-MISFETs. From 1995 to 2002, he worked in industry as an Engineer, Senior Engineer and Manager at Newport Wafer Fab. Ltd, Philips Semicond. Co. and Cavendish Kinetics Ltd respectively, developing CMOS and MEMS products and processes. From 2002, he became a Senior Researcher in Cambridge University in charge of MEMS group, and then became a professor at the Centre for Material Research and Innovation (CMRI), University of Bolton (BU) in 2007 to develop MEMS with main focus on microfluidics and lab-on-chip. He is the inventor on 8 patents and has published over 140 papers in peer reviewed journals and Int. conference proceedings. He has been a committee member of several int. conferences and has chaired and co-chaired many sessions. He is a guest editor for three editions of the journals on a special issue on MEMS and naomanufacturing, and the associated editor of the Chinese Journal of Sensors & Actuators.