

2010 - 2013

Invited Talks

1. A. G. Nassiopoulou, (invited talk) "Nanowires and Nanostructured Porous Si in RF", ESSDERC/ESSCIRC 2013, 43rd European Solid-State Device Research Conference, Bucharest, Romania, 16-20 September 2013
2. 3. A. G. Nassiopoulou, (invited talks) "Nanosilicon as a thermal isolation platform for thermoelectric and cooling devices", International Congress on Materials and Renewable Energy, Athens, 1-3 July 2013
3. 4. A. G. Nassiopoulou, (invited talk) "Si Nanowires by Chemistry or Electrochemistry: Formation, properties and applications", 4th IC4N-2013: The fourth International Conference from Nanoparticles and Nanomaterials to Nanodevices and Nanosystems, Corfu Island, 16-21 June 2013
4. 5. A. G. Nassiopoulou, (invited talk) "Porous Si platform for the on-chip integration of RF passive devices" 10th International Conference on Nanosciences & Nanotechnologies, NN13 Porto Palace Conference Centre & Hotel, Thessaloniki, Greece, 9 - 12 July 2013
5. 6. A. G. Nassiopoulou, (invited talk) "Nanostructured Si for Electronics and Sensors", 2nd Workshop on Modern Circuits and System Technologies, Thessaloniki 1st February 2013
6. 7. A. G. Nassiopoulou, (invited talk) "Porous Si platform for the on-chip integration of RF passive devices", 7th International Workshop on "Functional Nanomaterials and Devices", Kiev, Ukraine, 8 -11 April 2013
7. 8. S. Gardelis, P. Manousiadis, and A.G. Nassiopoulou, (invited talk) "Silicon Nanostructures: Optical and Optoelectronic Properties-Potential Applications In 3rd Generation Photovoltaics", The 2nd International Conference of the Serbian Ceramic Society, 2013
8. "Porous Si substrate technology for RF passives integration" A. G. Nassiopoulou, (invited), Workshop on Nanoelectronic Materials and Devices, organized by Micro&Nano, Athens, 17 December 2012
9. "High performance RF passive devices in CMOS technology on a local porous Si substrate", A. G. Nassiopoulou, (invited) Workshop on Recent Developments in Nanoelectronics, Thessaloniki, 3 June, 2012
10. "Nanostructured Si in the "More than Moore" Nanoelectronics field", A. G. Nassiopoulou, XI International Conference on Nanostructured Materials, 26-31 August 2012, Rhodes Greece (invited)
11. "Nanocrystalline systems and porous materials on Si for Electronics applications", A. G. Nassiopoulou, (invited talk), XXVII Panhellenic Conference on Solid State Physics and Materials Science, Limassol, Cyprus, 18-21 September 2011
12. " Nanowires and nanostructured porous Si for novel applications in Electronics and photovoltaics", A. G. Nassiopoulou, (invited talk), 3rd International Conference IC4N, held in Crete island, Greece, 26-29 June 2011
13. Low-dimensional materials for applications in Nanoelectronics, photovoltaics and sensors, A. G. Nassiopoulou, (invited talk), Workshop on the "Fabrication and characterization of nanomaterials and their applications", organized by the "Micro&Nano" Scientific Society, Thessaloniki, 3 March 2011

14. "High performance passive RF devices on a local porous Si substrate on the Si wafer", A. G. Nassiopoulou, (invited talk), Workshop on "Current trends in Nanoelectronics" organized by the "Micro&Nano" Scientific Society and the Nano-dema Network, Patras, 25 February 2011
15. "Nanowires, nanodots and nanoporous materials for applications in Nanoelectronics and Sensors", A. G. Nassiopoulou, Invited colloquium, University of Ioannina on the 5th of April 2011
16. "Electronic devices using porous anodic aluminum oxide" Hourdakis E. and Nassiopoulou A.G., Invited talk presented by M. Hourdakis, International Conference Nanomeeting 2011, Minsk, Belarus
17. "High-density MIM capacitors with porous anodic alumina dielectric", Hourdakis, E., Nassiopoulou, A.G, IEEE Transactions on Electron Devices 57 (10), art. no. 5535075, pp. 2679-2683 (2010)
18. "Photoluminescence-induced oscillations in porous anodic aluminum oxide films grown on Si: Effect of the interface and porosity", Gardelis, S., Nassiopoulou, A.G., Gianneta, V., Theodoropoulou, M., Journal of Applied Physics 107 (11), art. no. 113104 (2010)
19. "Electrical and structural properties of ultrathin SiON films on Si prepared by plasma nitridation", E. Hourdakis, A. G. Nassiopoulou, A. Parisini, M. A. Reading, J. A. van den Berg, L. Sygellou, S. Ladas, P. Petrik, A. Nutsch, M. Wolf, and G. Roeder, J. Vac. Sci. Technol. B 29, 022201 (2011) (accepted in 2010)
20. "Charge-trapping MOS memory structure using anodic alumina charging medium", E. Hourdakis and A.G. Nassiopoulou, Microelectronics Engineering, accepted for publication (accepted in 2010)
21. "High performance MIM capacitor for RF using anodic alumina dielectric", E. Hourdakis and A.G. Nassiopoulou, Microelectronics Engineering, (accepted in 2010)